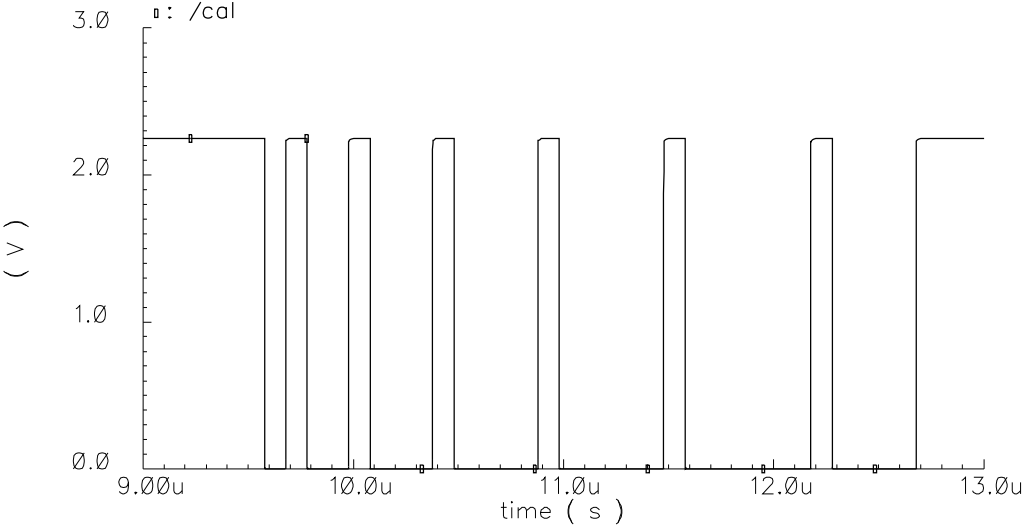
SIMULATE dectest schematic : Dec 5 08:03:40 2004

Transient Response