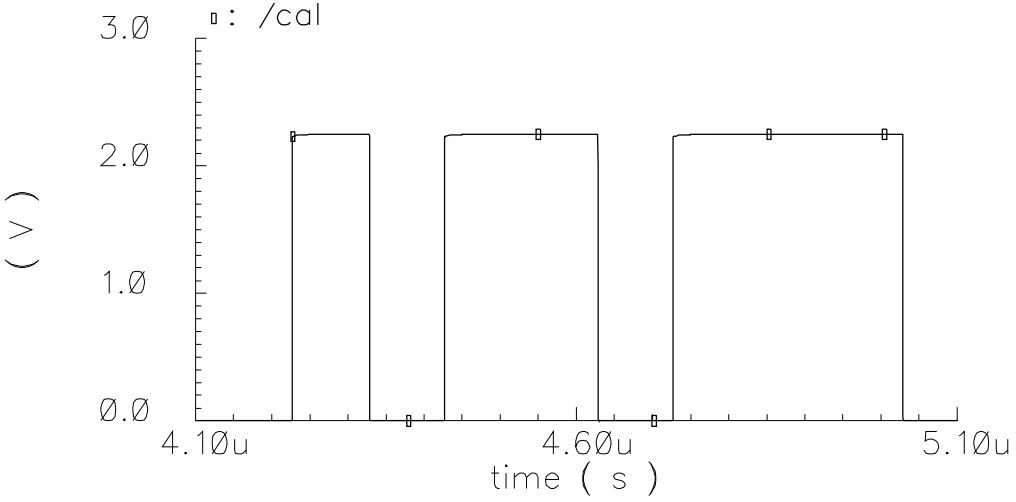
SIMULATE dectest schematic : Dec 5 16:19:12 2004

Transient Response