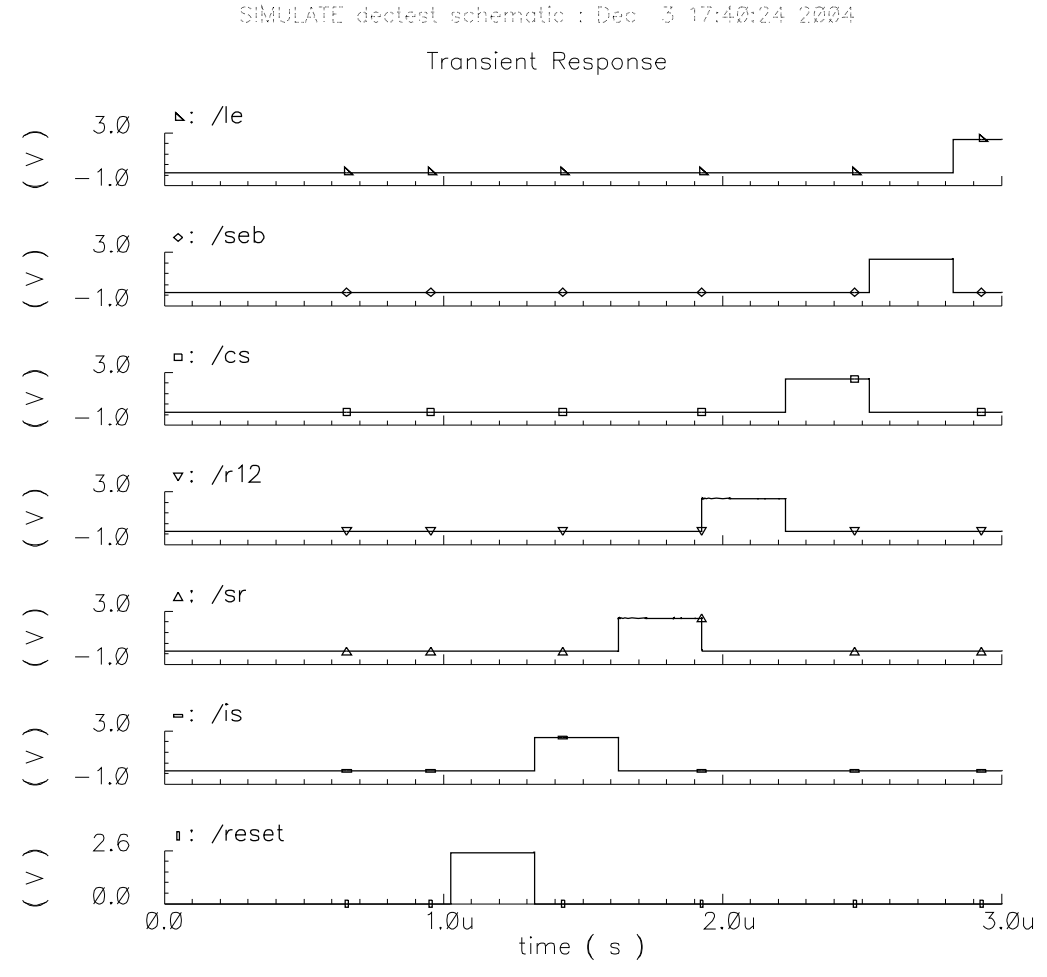
SIMULATE decTest schematic : Dec 3 17:40:24 2004
Transient Response
3.0
-1.0
(V)
: /le
: /seb
: /cs
: /r12
: /sr
: /is
2.6
0.0
: /reset
0.0
1.0u
2.0u
3.0u
time (s)