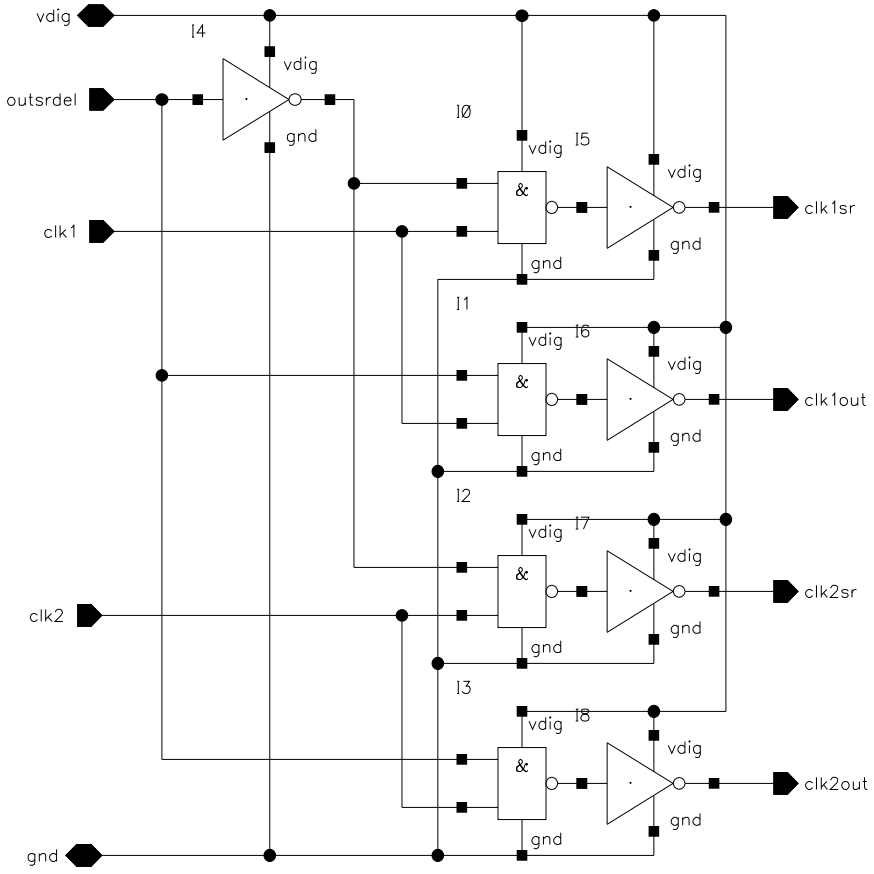
demux1


vdig
outsrdel
clk1
clk2
gnd
I4
I0
I1
I2
I3
I5
I6
I7
I8
&
clk1sr
clk1out
clk2sr
clk2out