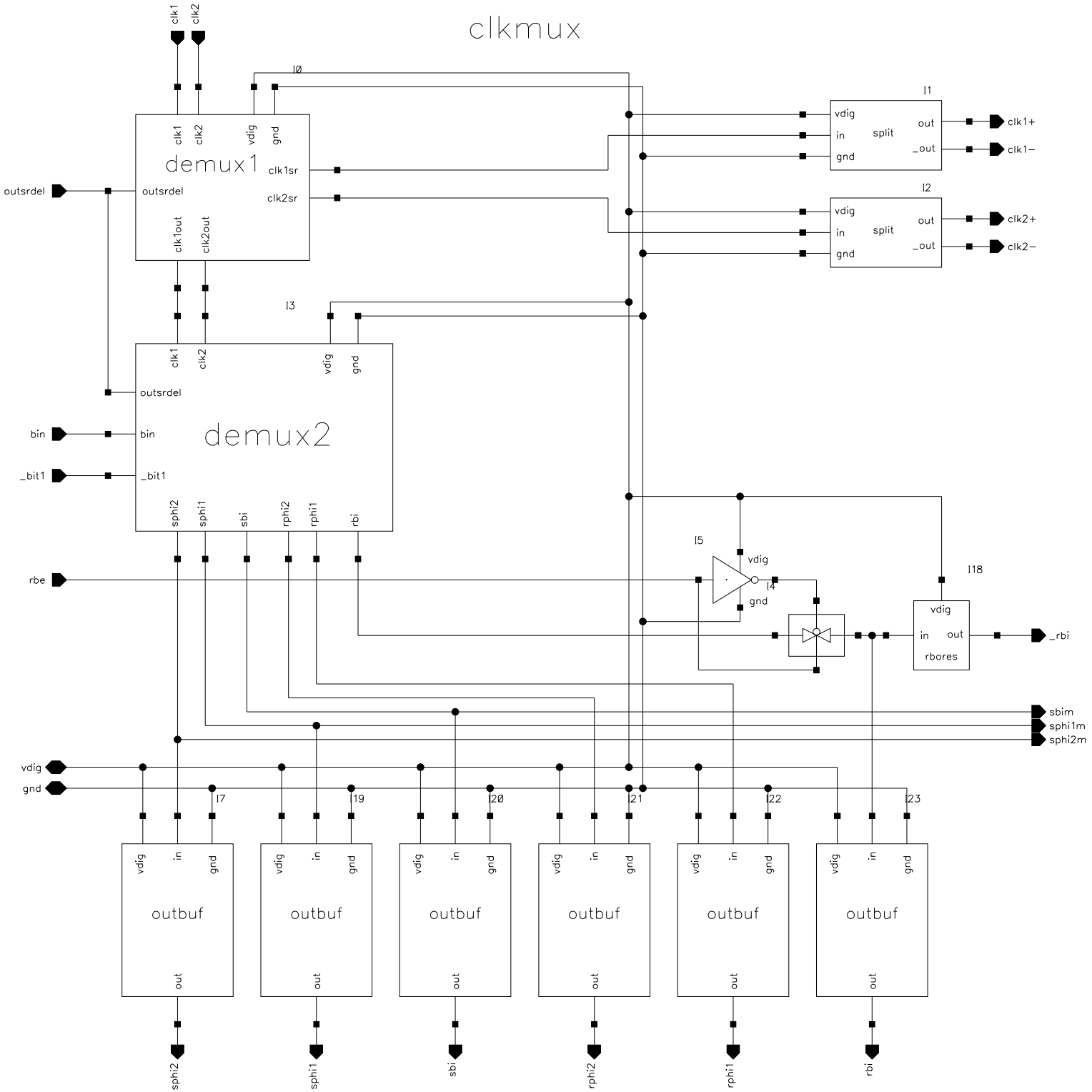
clkmux
clk1
clk2
I0
demux1
clk1
clk2
vdig
gnd
clk1sr
clk2sr
outsrdel
outsrdel
clk1out
clk2out
I1
vdig
in
gnd
split
out
_out
clk1+
clk1−
I2
clk2+
clk2−
I3
demux2
bin
_bit1
sphi2
sphi1
sbi
rphi2
rphi1
rbi
I5
I4
I18
rbe
rbores
_rbi
sbim
sphi1m
sphi2m
I7
I19
I20
I21
I22
I23
outbuf
out