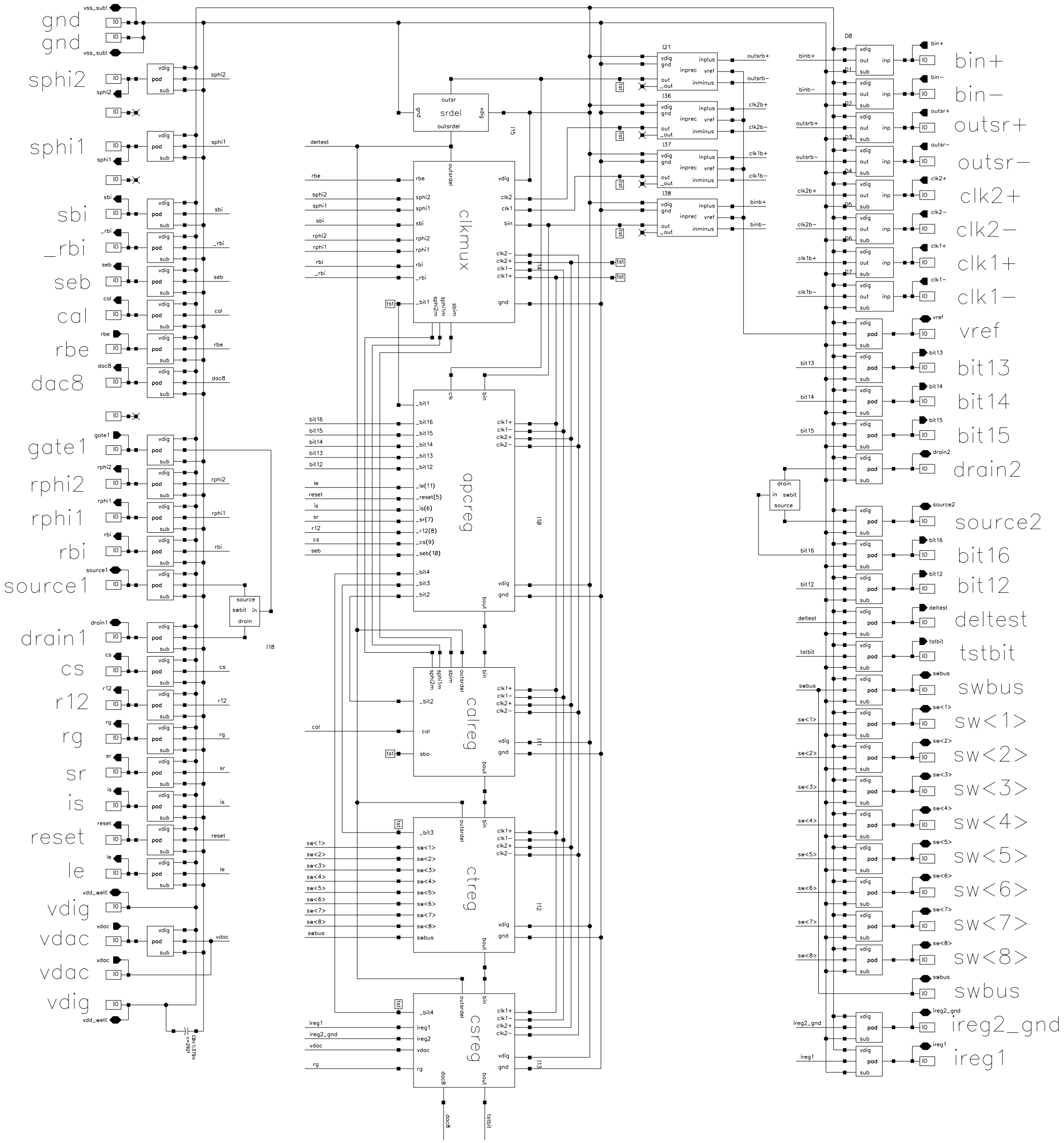
gnd
gnd
sphi2
sphi1
sbi
_rbi
seb
cal
rbe
dac8
gate1
rphi2
rphi1
rbi
source1
drain1
cs
r12
rg
sr
is
reset
le
vdig
vdac
vdac
vdig
clkmux
srdel
apcreg
calreg
ctreg
csreg
bin+
bin−
outsr+
outsr−
clk2+
clk2−
clk1+
clk1−
vref
bit13
bit14
bit15
drain2
source2
bit16
bit12
deltest
tstbit
swbus
sw<1>
sw<2>
sw<3>
sw<4>
sw<5>
sw<6>
sw<7>
sw<8>
swbus
ireg2_gnd
ireg1