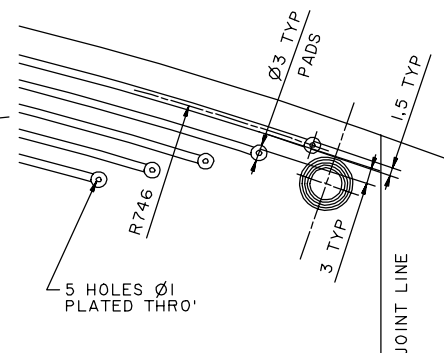
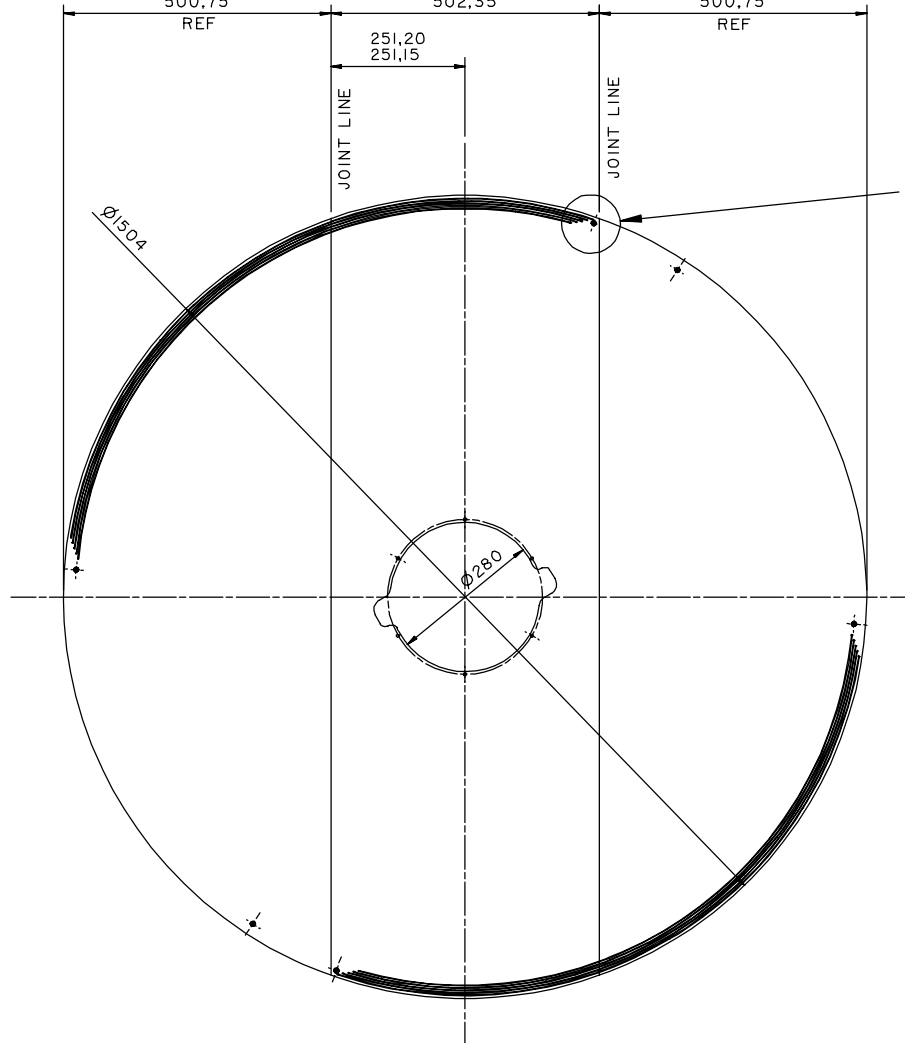


Diagram illustrating the layout of a 32-bit floating-point number (IEEE 754 standard):

- Sign (S): 1 bit
- Exponent (E): 8 bits
- Fraction (F): 23 bits

The diagram shows the bit positions for the sign, exponent, and fraction fields, with the sign field being the most significant bit and the fraction field being the least significant bits.



TYPICAL
CONDUCTOR PATTERN
DETAILS
SCALE 2:1

FOR ALL HOLE & CUTOUT DETAILS
REF DRG NO 119/10030.

1. ARTWORK TO BE EMULSION UP POSITIVE.
2. THIS REVERSE PATTERN TO BE ALIGNED WITH
OBSERVE PATTERN (REF DRG NO 119/10030)
TO WITHIN 0.05 MM.
3. ALL COPPER TRACKS TO BE 18 MICRON
THICKNESS AND UNTINNED.
4. TRACK DETAIL INFORMATION AVAILABLE
IN EITHER DXF OR HPGL FORMAT FOR
MANUFACTURING PURPOSES.
5. GENERAL DIMENSIONAL TOLERANCES
TO APPLY TO EACH OF THE 3 PCB'S.

FOR OBVERSE PCB TRACK DRAWING
REF DRG NO 119/10030.
FOR SLOT MACHINING DETAIL DRAWING
REF DRG NO 119/10032.

MATERIAL	0,5 MM THICK 18 MICRON DOUBLE SIDED COPPER CLAD PCB
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TOLERANCES UNLESS OTHERWISE STATED
 LINEAR DIMENSIONS UP TO 100 $\pm 0,05$: OVER 100 $\pm 0,1$
 ANGULAR DIMENSIONS $\pm 0,1^\circ$

SCALE 1:5



0 500

DRN.	GS	COUNCIL FOR THE CENTRAL LABORATORY OF THE RESEARCH COUNCILS DARESBUURY LABORATORY CHESHIRE
DATE.	17/11/98	

CRD.	GS	TITLE
DATE.	17/11/98	
CERT.	GS	

HI PLANAR CHAMBER
REVERSE PCB TYPE A

DATE.	17/11/98	DRAWING N°
APPD.	GH	A1-119/10031

ISSUE
2

CONTRACTORS COMPANY

CONTRACTORS DRG N°

A	FIRST ISSUE	
	DRN	GS
	DATE	3/2/99
	CERT	GS
B	APPD	GH
	PCB TRACK THICKNESS CHANGED. NOTE 5 ADDED.	