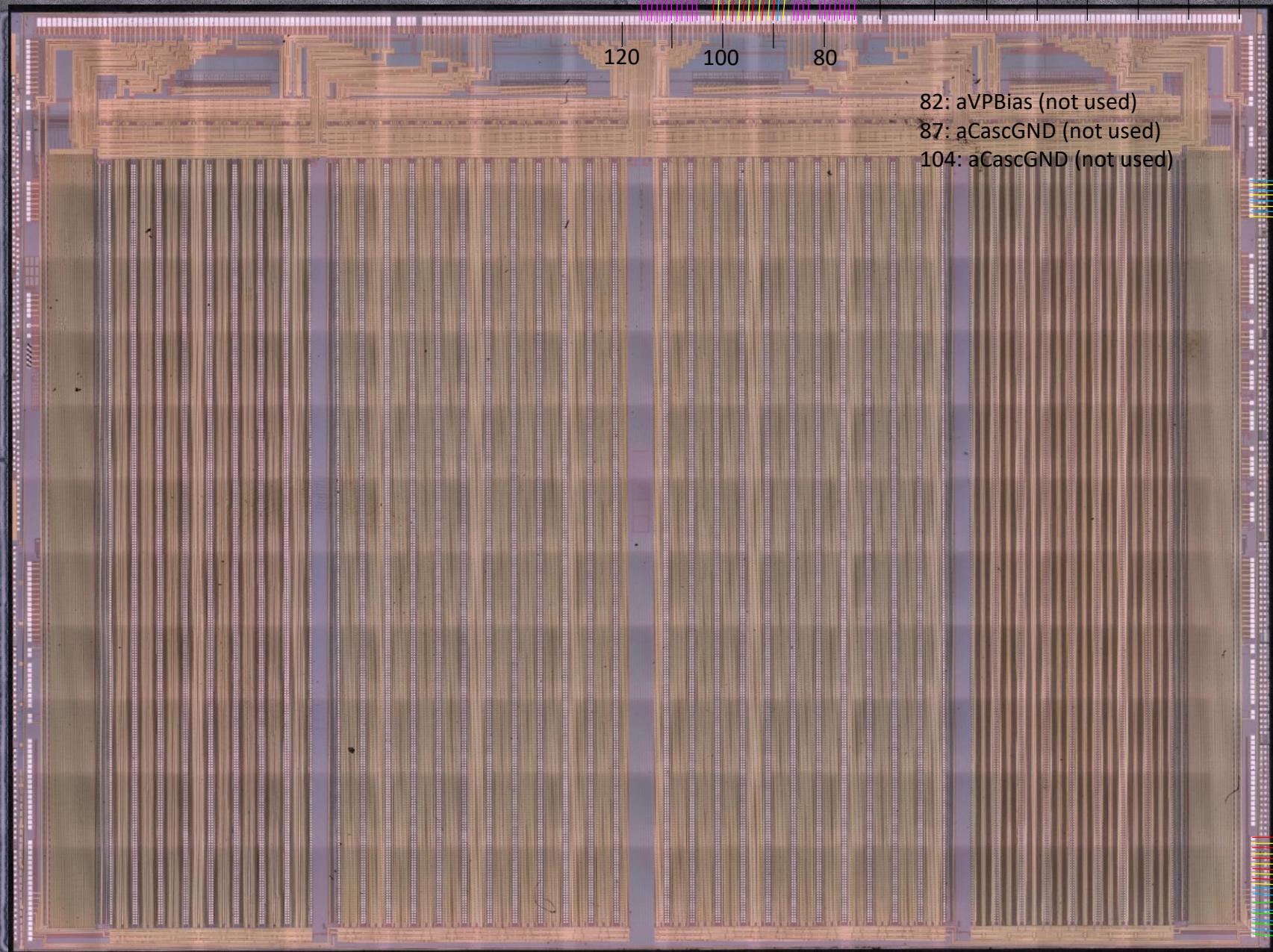


13/07/2020



VSSA

VDDA

VDD

GNDD

8

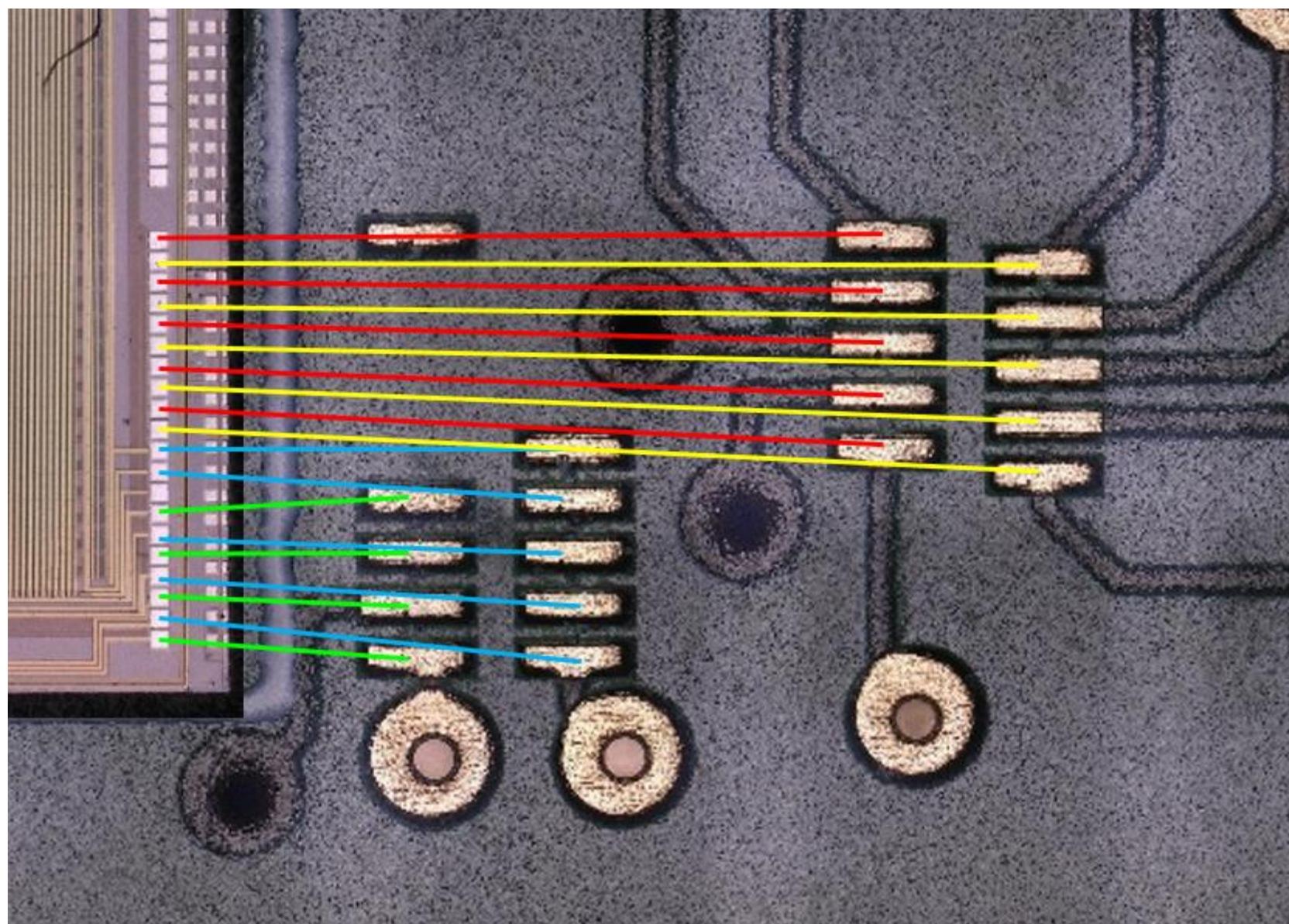
120 100 80

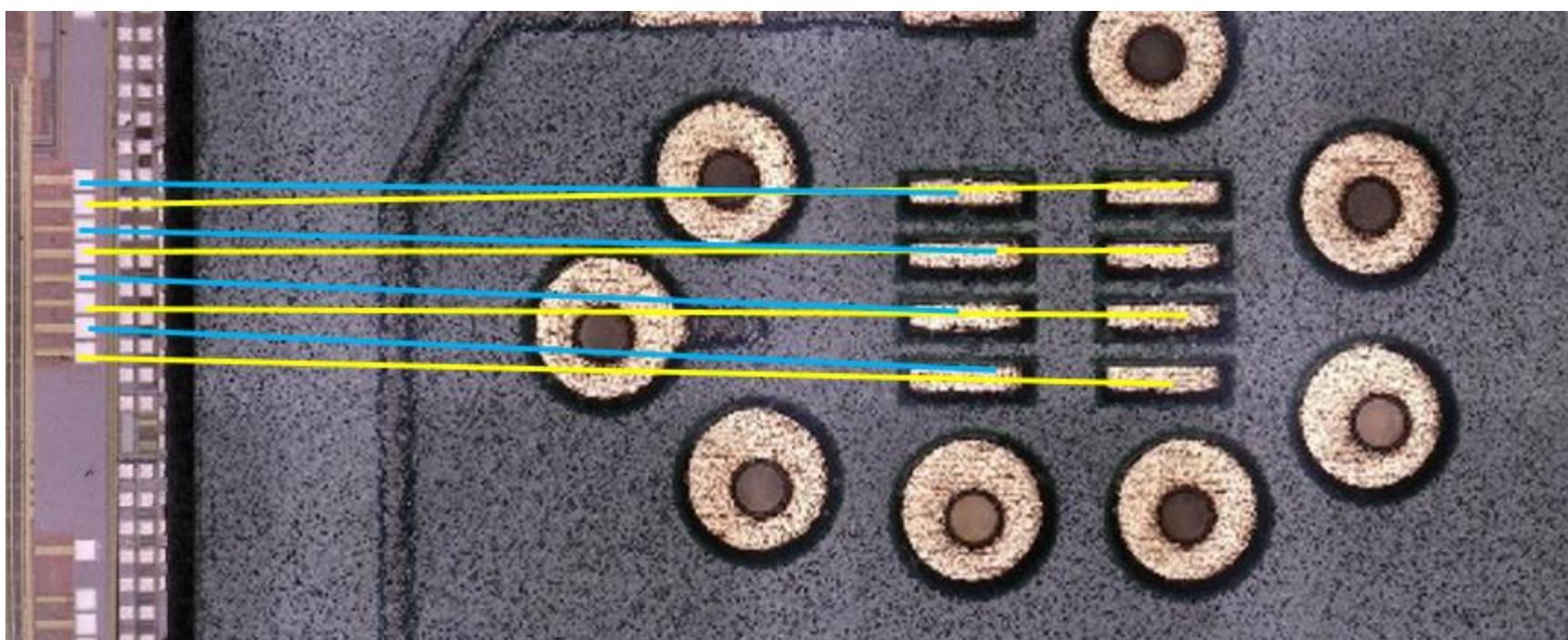
82: aVPBias (not used)
87: aCascGND (not used)
104: aCascGND (not used)

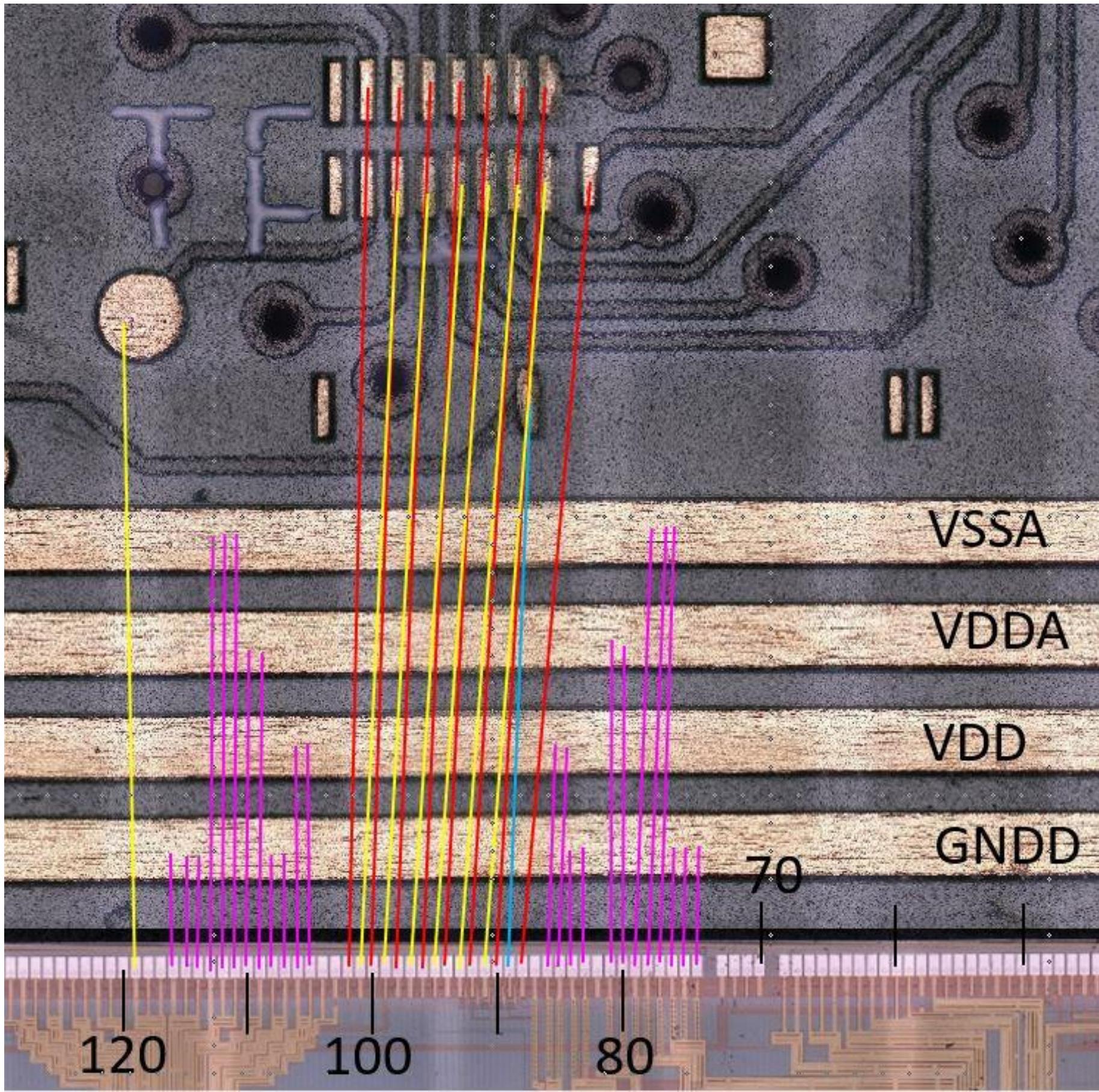
70

0

30







First analog matrix:

Pad number	Pad name	Purpose	Pad number	Pad name	Purpose
L73 (e)	Sub	HV, pixel ring	L95 (c)	aSIn	Dig in CMOS
(d)	No bonding	Separation	L96 (c)	aLd	Dig in CMOS
L74 (b)	gnda!	Power ana, protection	L97 (c)	aCk2	Dig in CMOS
L75 (c)	gnda!	Power ana	L98 (c)	aCk1	Dig in CMOS
L76 (c)	gnda!	Power ana	L99 (c)	aSensBias	Ana in
L77 (c)	vssa!	Ana in	L100 (c)	aVGate	Ana in
L78 (c)	vssa!	Ana in	L101 (c)	aTh	Ana out (1.5 V)
L79 (c)	vssa!	Ana in	L102 (c)	aBL	Ana out (1.5 V)
L80 (a)	vdda!	Power ana, protection	L103 (c)	aVCascPMOS	Ana out (1.6 V)
L81 (c)	vdda!	Power ana	L104 (c)	aCascGND	Ana in, not used
L82 (c)	aVPBias	Ana in, main bias: decoupling or n.c.	L105 (c)	vddd!	Power dig pixel
L83 (c)	gndd!	Power dig pixel	L106 (c)	vddd!	Power dig pixel
L84 (c)	gndd!	Power dig pixel	L107 (c)	gndd!	Power dig pixel
L85 (c)	vddd!	Power dig pixel	L108 (c)	gndd!	Power dig pixel
L86 (c)	vddd!	Power dig pixel	L109 (c)	vdda!	Power ana
L87 (c)	aCascGND	Ana in, not used	L110 (a)	vdda!	Power ana, protection
L88 (c)	alnjection	Signal in	L111 (c)	vssa!	Ana in
L89 (c)	AnalogOut_1	Ana out	L112 (c)	vssa!	Ana in
L90 (c)	aMonitor<0>	Ana out	L113 (c)	vssa!	Ana in
L91 (c)	aMonitor<1>	Ana out	L114 (c)	gnda!	Power ana
L92 (c)	aMonitor<2>	Ana out	L115 (c)	gnda!	Power ana
L93 (c)	aSOut	Dig out CMOS	L116 (b)	gnda!	Power ana, protection
L94 (c)	aShiftEnB	Dig in CMOS or '0'			

Table VII Pads on the left hand side connected to the first analog matrix.

Second analog matrix:

Pad number	Pad name	Purpose	Pad number	Pad name	Purpose
L117 (b)	gnda!	Power ana, protection	L139 (c)	bShiftEnB	Dig in CMOS or '0'
L118 (c)	gnda!	Power ana	L140 (c)	bSOut	Dig out CMOS
L119 (c)	gnda!	Power ana	L141 (c)	bMonitor<2>	Ana out
L120 (c)	vssa!	Ana in	L142 (c)	bMonitor<1>	Ana out
L121 (c)	vssa!	Ana in	L143 (c)	bMonitor<0>	Ana out
L122 (c)	vssa!	Ana in	L144 (c)	AnalogOut_2	Ana out
L123 (a)	vdda!	Power ana, protection	L145 (c)	bInjection	Signal in
L124 (c)	vdda!	Power ana	L146 (c)	bCascGND	Ana in
L125 (c)	gndd!	Power dig pix	L147 (c)	vddd!	Power dig pix
L126 (c)	gndd!	Power dig pix	L148 (c)	vddd!	Power dig pix
L127 (c)	vddd!	Power dig pix	L149 (c)	gndd!	Power dig pix
L128 (c)	vddd!	Power dig pix	L150 (c)	gndd!	Power dig pix
L129 (c)	bCascGND	Ana in	L151 (c)	bVPBias	Ana in
L130 (c)	bVCascPMOS	Ana out, not used	L152 (c)	vdda!	Power ana
L131 (c)	bBL	Ana out (1.5 V)	L153 (a)	vdda!	Power ana, protection
L132 (c)	bTh	Ana out (1.5 V)	L154 (c)	vssa!	Power ana
L133 (c)	bVGate	Ana in	L155 (c)	vssa!	Power ana
L134 (c)	bSensBias	Ana in	L156 (c)	vssa!	Power ana
L135 (c)	bCk1	Dig in CMOS	L157 (c)	gnda!	Power ana
L136 (c)	bCk2	Dig in CMOS	L158 (c)	gnda!	Power ana
L137 (c)	bLd	Dig in CMOS	L169 (b)	gnda!	Power ana, protection
L138 (c)	bSIn	Dig in CMOS	(d)	No bonding	Separation
			L160 (e)	Sub	HV, pixel ring

Table VIII Pads on the left hand side connected to the second analog matrix.