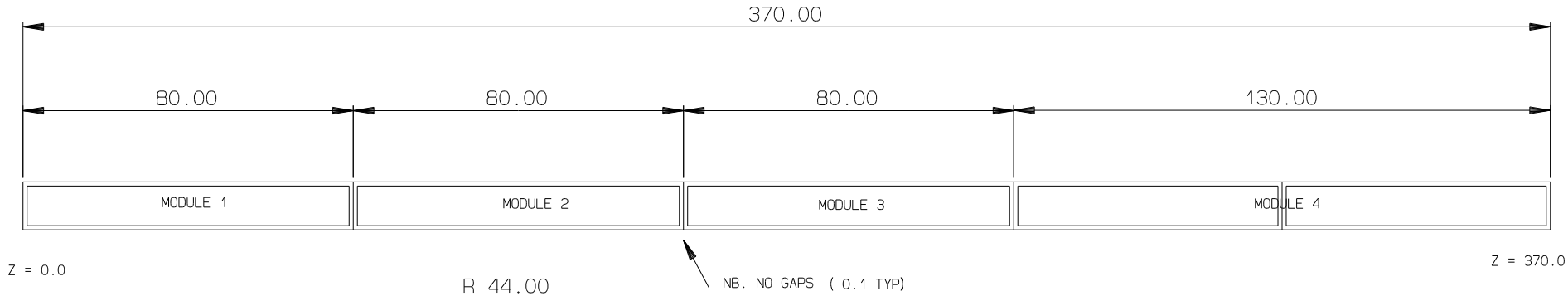

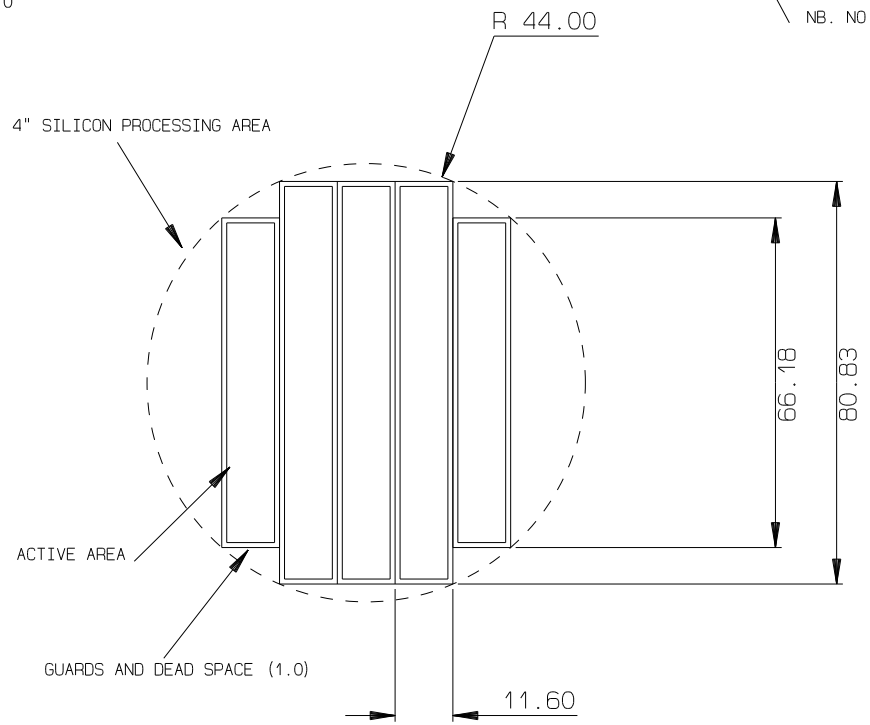

NOTES

EACH HALF-LADDER HAS FOUR MODULES

MODULES 1, 2 AND 3 ARE 80 MM LONG

MODULE 4 IS 130 MM LONG

ONE FOUR MODULE HALF-LADDER IS FORMED FROM ONE WAFER

THERE ARE 12 MODULES PER REV.

THE TOTAL NUMBER OF MODULES IS 4 X 12 X 2 = 96

THE TOTAL NUMBER OF ASICS IS 96

THE TOTAL NUMBER OF SILICON WAFERS IS 12 X 2 = 24

EACH SENSOR HAS 128 ACTIVE STRIPS

THE STRIP PITCH IS 0.075 MM

THE DEAD REGION AT THE EDGE IS 1.0 MM WIDE

UNIVERSITY OF LIVERPOOL	CDF / LAYER 00	NB. ALL DIMENSIONS ARE MM			Scale		Title LAYER 00 LADDER STRUCTURE	Drawn by	T.J.JONES
					Date	19/10/98		Dwg No.	CDF-001-A