



Only the first 4 of the 50 integrator channels are drawn to avoid repetition  
 The remaining channels are identical and component IDs increment as here

- NOTES
- 1) Inductors from L101 are shorted with a trace on the solder side of the board - these can be cut if the inductors are needed
  - 2) the 30k resistor in series with the input is a bandwidth limiter.
  - 3) the 100k resistor in series with the calibrator capacitor limits the maximum input current to <math>100\mu A</math>
  - 4) the cal signal potentiometer should be initially set to mid point and adjusted to give 9.45v output from the board
  - 5) No external integrator capacitors are installed - add only if input signal is too large